

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

Applicant: Chambers Docket No: TI-30883  
Serial No: 09/902,051 Examiner: Davidson, Dan  
Filed: 7/10/2001 Art Unit: 2651  
For: CMOS DC OFFSET CORRECTION CIRCUIT WITH PROGRAMMABLE HIGH-PASS TRANSFER FUNCTION

**AMENDMENT UNDER 37 C.F.R. § 1.115**

Assistant Commissioner For Patents  
Washington, DC 20231

Dear Sir:

Responsive to the Office Action mailed December 5, 2005 in connection with the above identified application, Applicants respectfully submit the following amendments and remarks.